

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/678,751	10/03/2000	John B. Halbert	5038-63	5518	
20575 75	11/20/2002		EXAM	INER	
MARGER JOHNSON & MCCOLLOM PC 1030 SW MORRISON STREET			VITAL, PIERRE M		
	PORTLAND, OR 97205		ART UNIT	PAPER NUMBER	
			2188		
			DATE MAILED: 11/20/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.





UNITED STATES PEPARTMENT OF COMMERCE U.S. Patent and Trademark Office

DATE MAILED:

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION		ATTORNEY DOCKET NO.	
			EXAMINER		
			ART UNIT	PAPER	
				11	

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner for Patents

The Supplemental Information Disclosure Statement (IDS) filed 9/29/03 and submitted under 37 CFR 1.97 (c) has not been considered by the Examiner because it does not contain a statement under 37 CFR 1.97 (e) as required when filed after final action or notice or allowance.

REGINALD G. BRAGDON PRIMARY EXAMINER

·.	Application No.	Applicant(s)				
∨ Notice of Allowability	09/678,751	HALBERT ET AL.				
Notice of Anonability	Examiner	Art Unit				
	Pierre M. Vital	2188				
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOT of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to	plication. If not include will be mailed in due	ed course. THIS			
 This communication is responsive to <u>10/6/03</u>. The allowed claim(s) is/are <u>1, 4, 5, 7-14, 17-19, 15-16, 20,</u> 	22 24-25 renumbered 1-20					
3. The drawings filed on <u>03 October 2000</u> are accepted by th						
4. ☐ Acknowledgment is made of a claim for foreign priority und a) ☐ All b) ☐ Some* c) ☐ None of the:						
 Certified copies of the priority documents have 	been received.					
2. Certified copies of the priority documents have	been received in Application No	·				
 Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). 	cuments have been received in this	national stage applica	tion from the			
* Certified copies not received:						
5. Acknowledgment is made of a claim for domestic priority u	- , , , ,	onal application).				
(a) The translation of the foreign language provisional a	` '	•				
Acknowledgment is made of a claim for domestic priority u	nder 35 U.S.C. 99 120 and/or 121.					
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE						
7. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.						
8. CORRECTED DRAWINGS must be submitted. (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No		,				
(b) ☐ including changes required by the proposed drawing correction filed, which has been approved by the Examiner.(c) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No						
(c) I including changes required by the attached Examiner	s Amendment / Comment or in the t	Office action of Paper	NO			
Identifying indicia such as the application number (see 37 CFR 1 each sheet.	.84(c)) should be written on the drawin	ngs in the front (not the	back) of			
9. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT FOR T			Note the			
Attachment(s)						
 1 Notice of References Cited (PTO-892) 3 Notice of Draftperson's Patent Drawing Review (PTO-948) 5 Information Disclosure Statements (PTO-1449), Paper No 	4⊠ Interview Summ	al Patent Application (ary (PTO-413), Paper Indment/Comment				
7 Examiner's Comment Regarding Requirement for Deposit of Biological Material	8∏ Examiner's State 9∏ Other .	ement of Reasons for	Allowance			
	•					

Application/Control Number: 09/678,751

Art Unit: 2188

Page 2 78

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with James E. Harris (Reg. No. 40,013) on October 9, 2003.

2. The application has been amended as follows:

In claim 4, line 1, after "claim 1,"; please insert --wherein--.

In claim 4, line 2, after "each"; please change "having" to -have--.

In claim 9, line 1, after "claim 5,"; please insert --wherein--.

In claim 9, line 1/2, after "controller", please insert -is--.

In claim 13, line 1, after "claim 11,"; please change "where" to -wherein--.

Please rewrite the abstract to read as follows:

--Methods and apparatus for a memory system using a new memory module architecture are disclosed. In one embodiment, the memory module has two ranks of memory devices, each rank connected to a corresponding one of two 64-bit-wide data registers. The data registers connect to two 64-bit-wide ports of a 120:64

(

C

Application/Control Number: 09/678,751

Art Unit: 2188

multiplexer/demultiplexer, and a 64-bit-wide data buffer connects to the opposite port of the multiplexer/demultiplexer. A controller synchronizes the operation of the data registers, the multiplexer/demultiplexer, and the data buffer. In an operating environment, the data buffer connects to a memory bus. When a data access is performed, both ranks exchange data signaling with their corresponding data registers during a single data access. At the buffer, the memory bus data transfer occurs in two consecutive clock cycles, one cycle for each rank. This allows the memory bus transfer rate to double for the same memory bus width and memory device speed--.

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre M. Vital whose telephone number is (703) 306-5839. The examiner can normally be reached on Mon-Fri, 8:30 am - 6:00 pm, alternate Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on (703) 306-2903. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Application/Control Number: 09/678,751

Art Unit: 2188

Page 4

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-9000.

Fierre M. Vital Art Unit 2188

October 9, 2003

REGINALD G. BRAGDON PRIMARY EXAMINER